

## **Understanding Latency in AoIP Systems**

## "Your Flight Will Be Delayed by 20 ms"

MoIP Pavilion @ AES New York 2022

Andreas Hildebrand, ALC NetworX





#### Latency in AoIP Systems





#### Andreas Hildebrand, RAVENNA Technology Evangelist

- more than 25 years in the professional audio / broadcasting industry
- graduate diploma in computer science
- R&D, project & product management experience
- member of AES67 TG and ST2110 DG

#### **ALC NetworX GmbH**, Munich / Germany

- established 2008
- R&D center
- developing & promoting RAVENNA
- Partnerships with > 40 manufacturers



**ALC** NetworX

#### RAVENNA

- IP media networking technology
- designed to meet requirements of professional audio / broadcasting applications
- open technology approach, license-free
- fully AES67- and SMPTE ST2110-compliant









- "... is a time **delay between** the **cause** and the **effect** of some physical change in the system being observed." [Wiki]
- "... is a **consequence** of the **limited velocity** at which any physical interaction can propagate [...] which is always less than the **speed of light**." [Wiki]

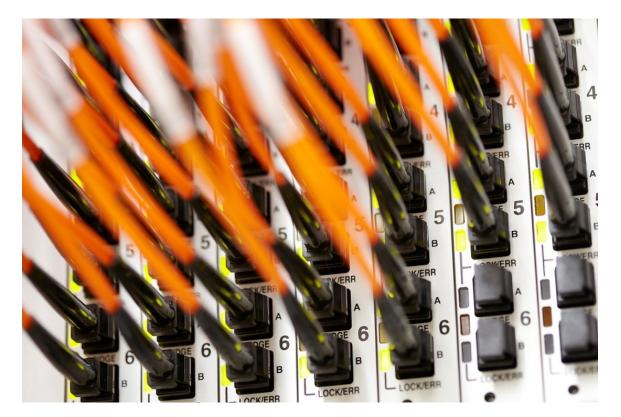






## (Traditional) Digital Signal Distribution



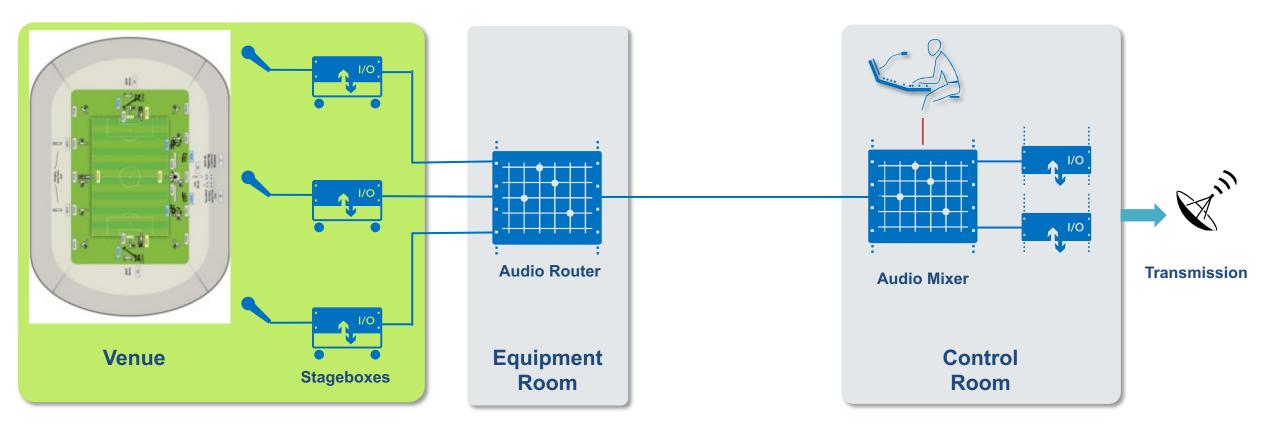








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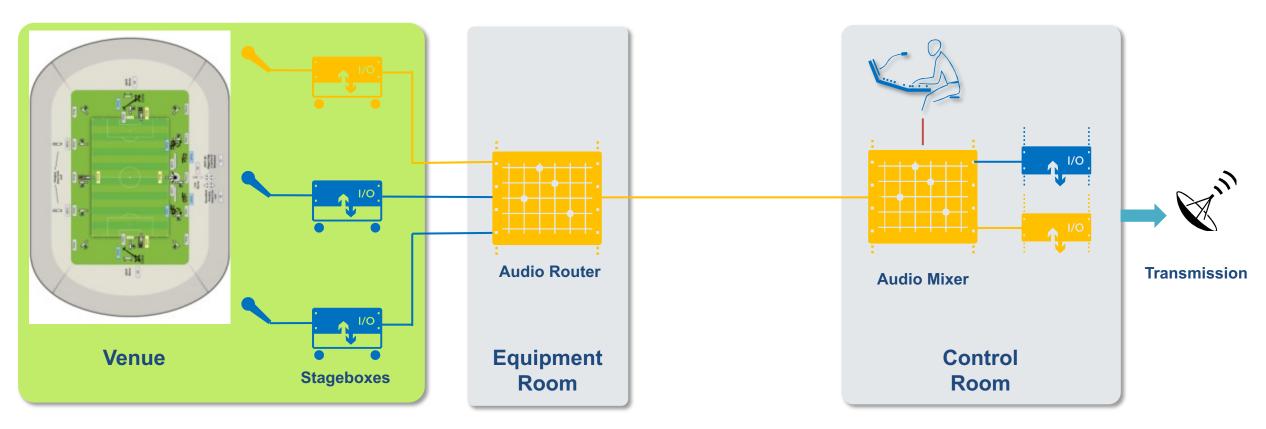




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#### (Traditional) Digital Signal Distribution



fixed connection between microphone and mixer output

fixed / deterministic latency
= circuit switched routing









#### Approximate light signal travel times

Distance	Time
1 m	3.3 ns
1 km	3.3 μs
4000 km (NYC – LA)	13.3 ms
to geostationary orbit	119 ms
around Earth's equator	134 ms
Moon to Earth	1.3 s
Proxima Centauri (nearest star)	4.2 years
across the Milky Way	100,000 years
Andromeda Galaxy (nearest galaxy)	2.5 million years





## **Networked Media Distribution**



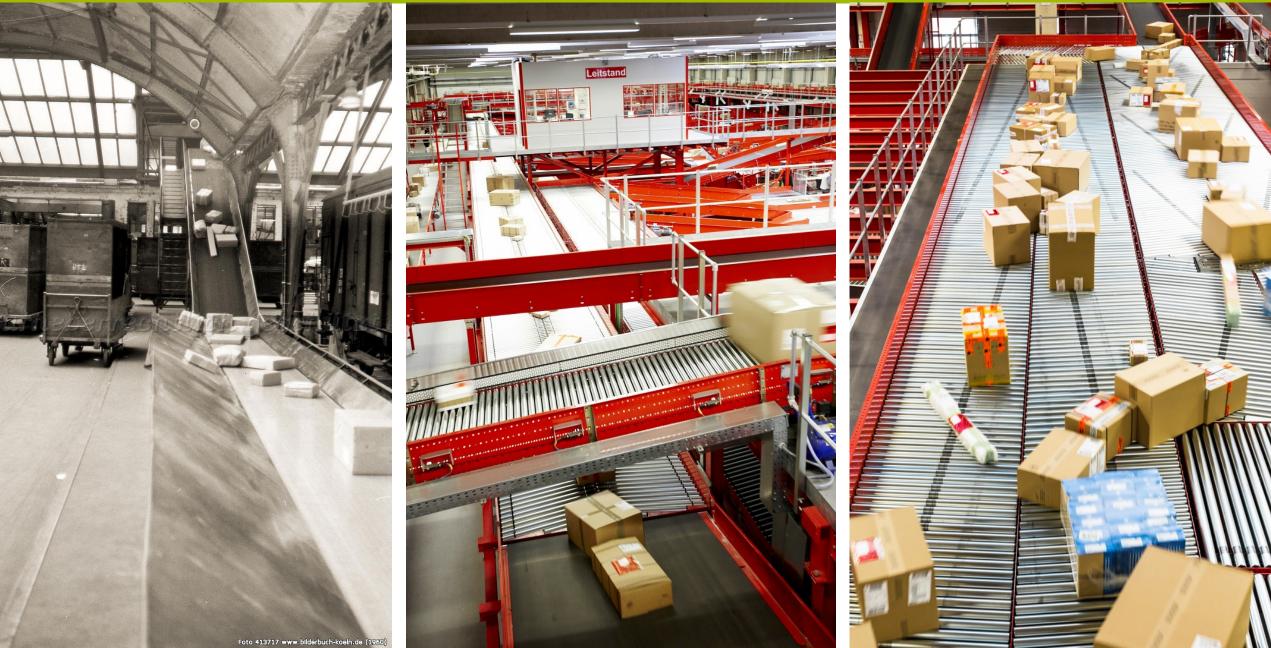
# **Packet Switching**



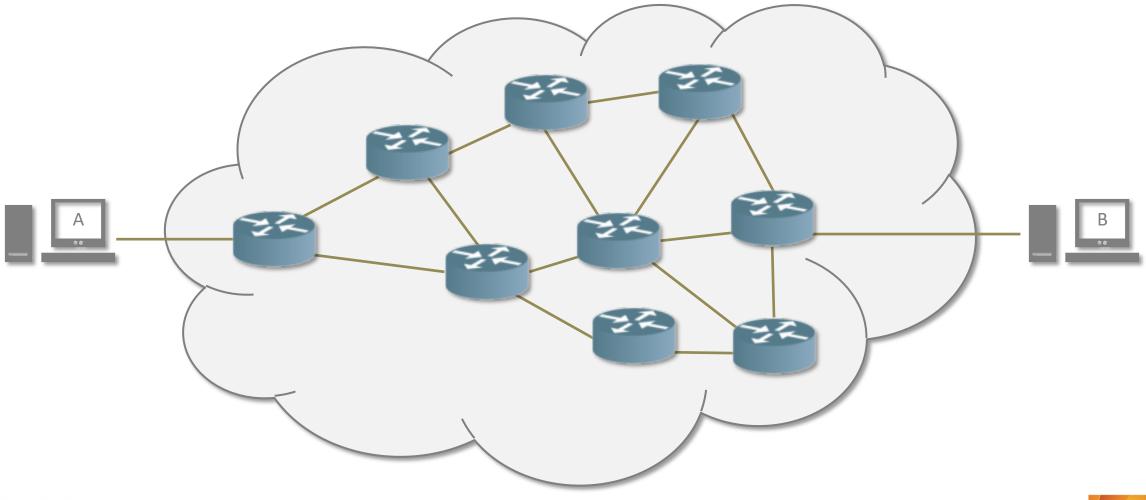


#### Latency in AoIP Systems







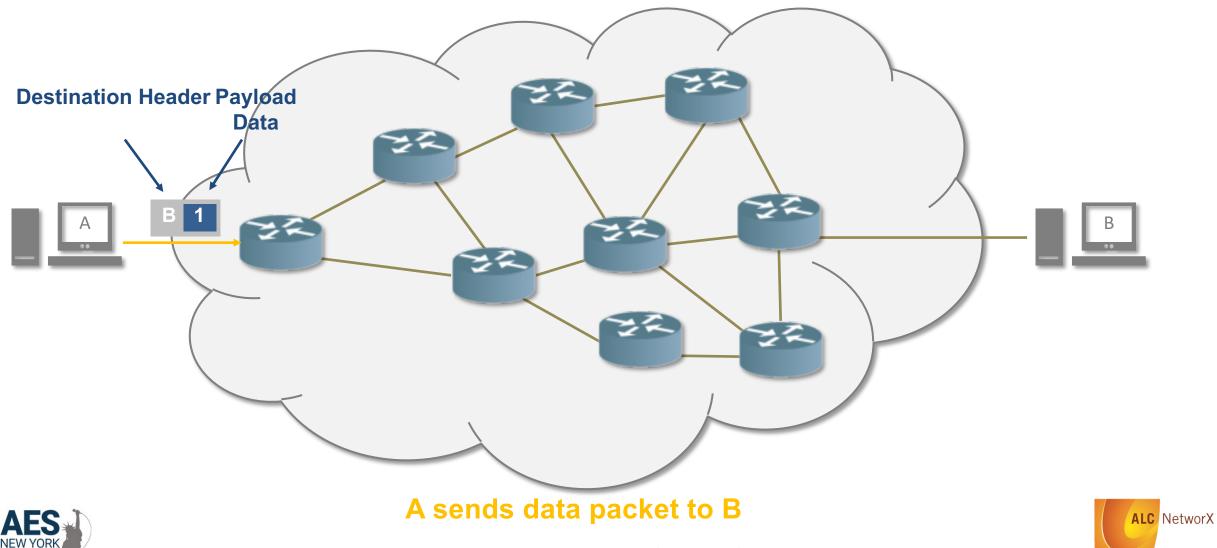




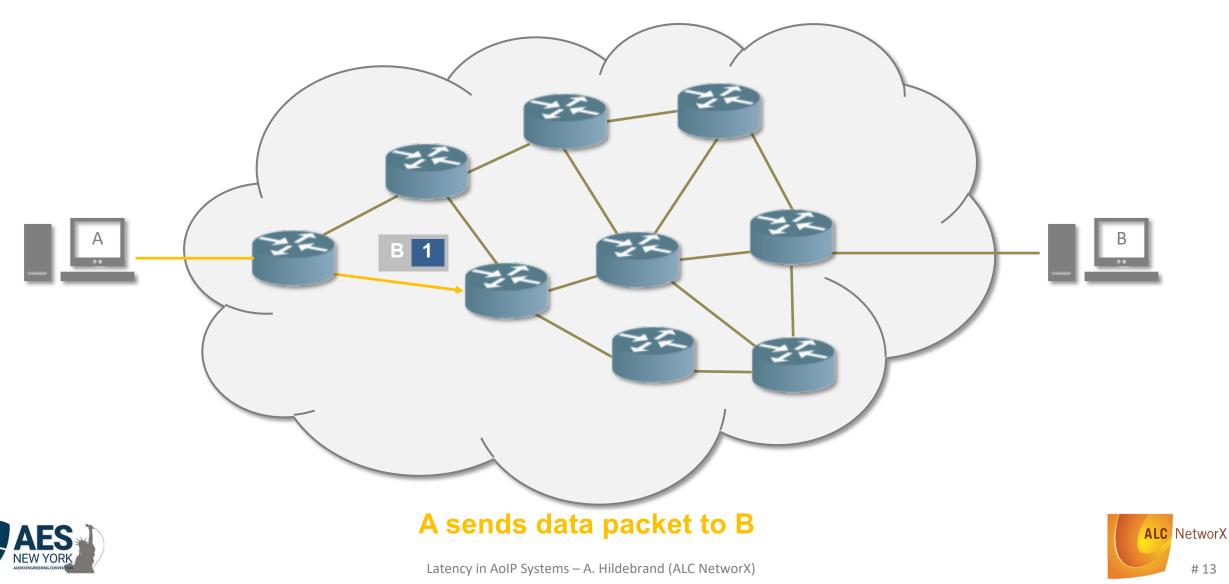




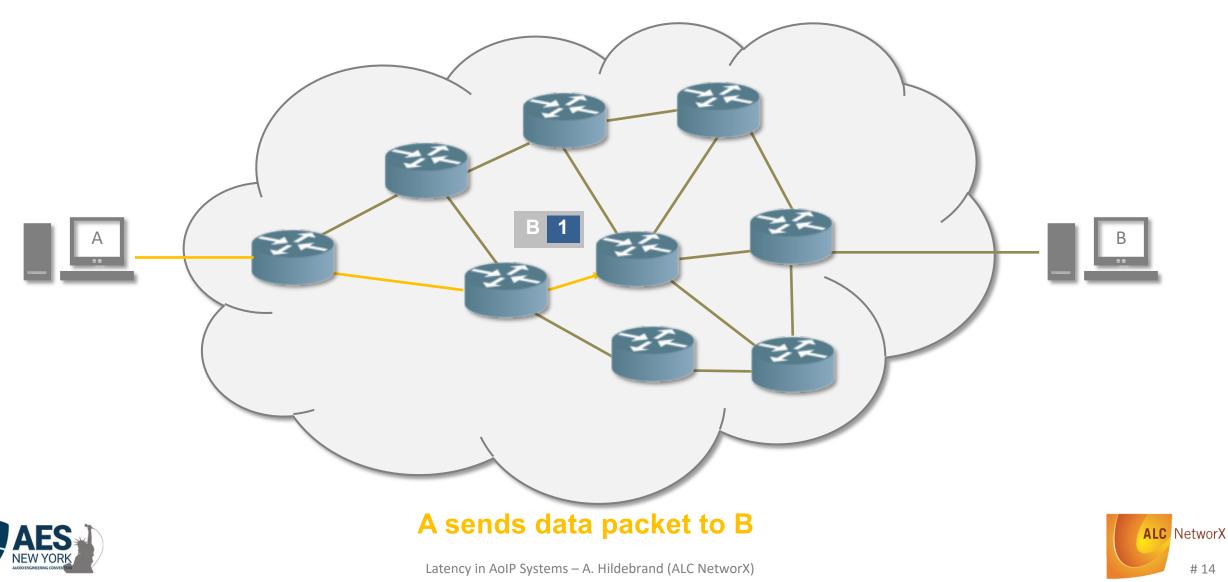
**IP** Packet Switching



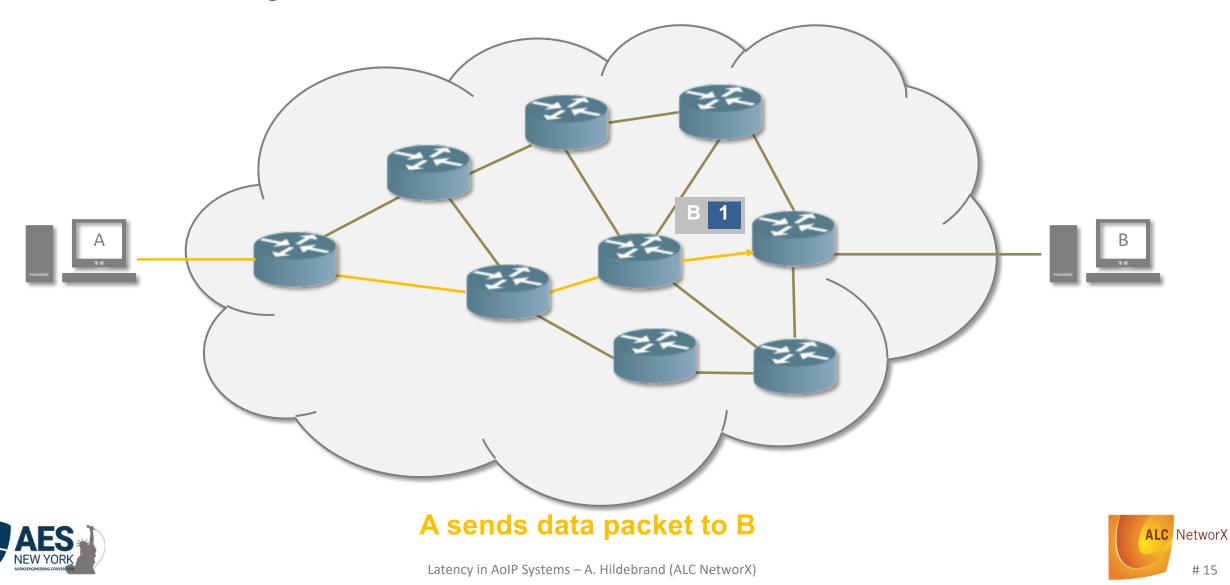




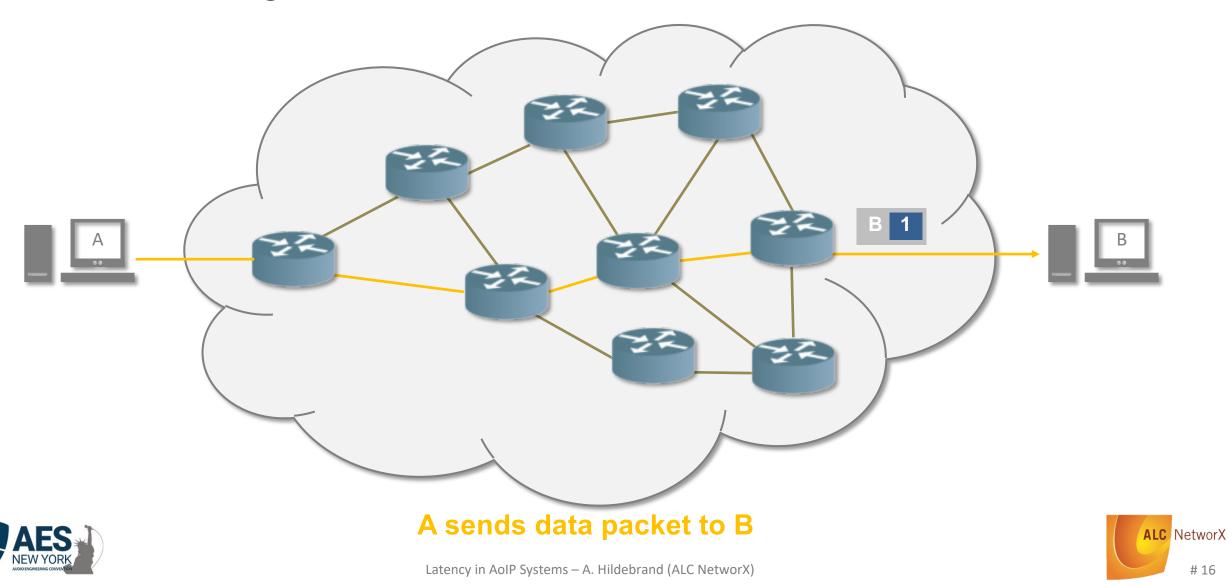






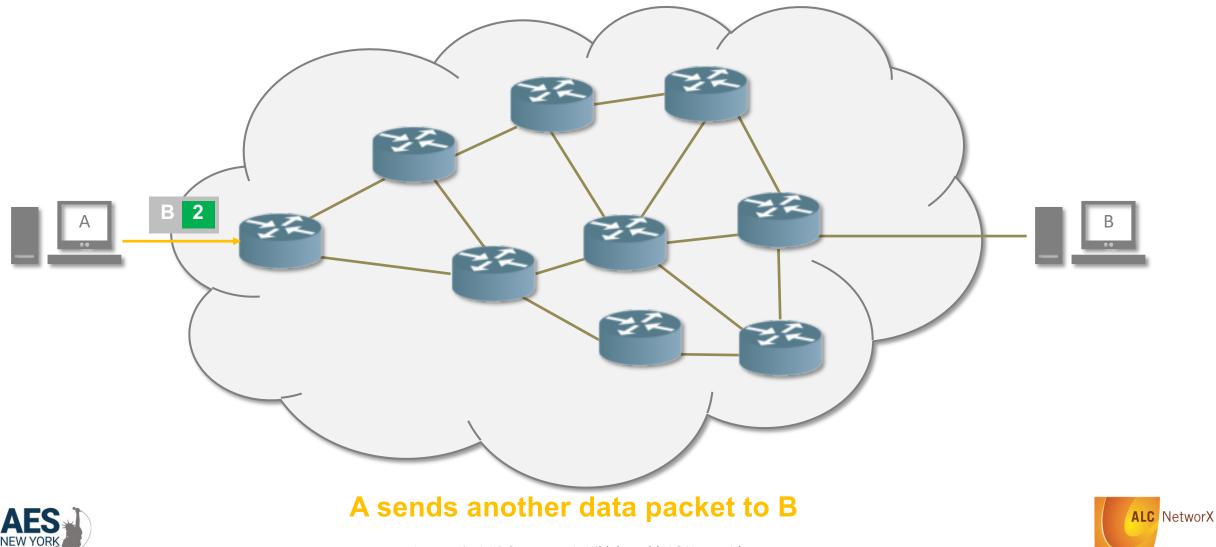






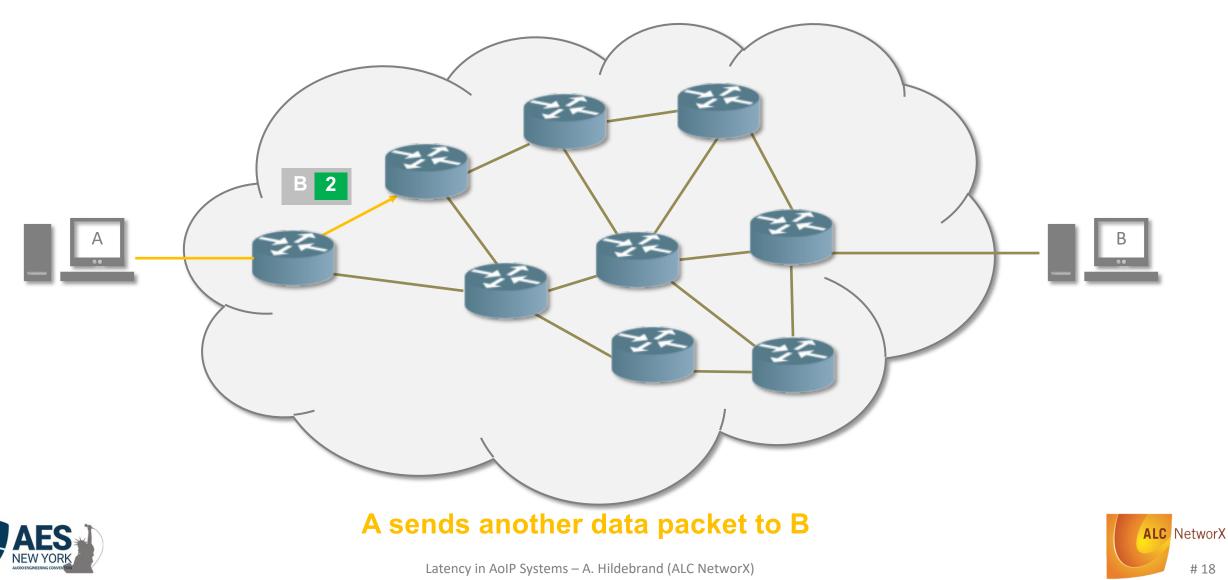


**IP Packet Switching** 





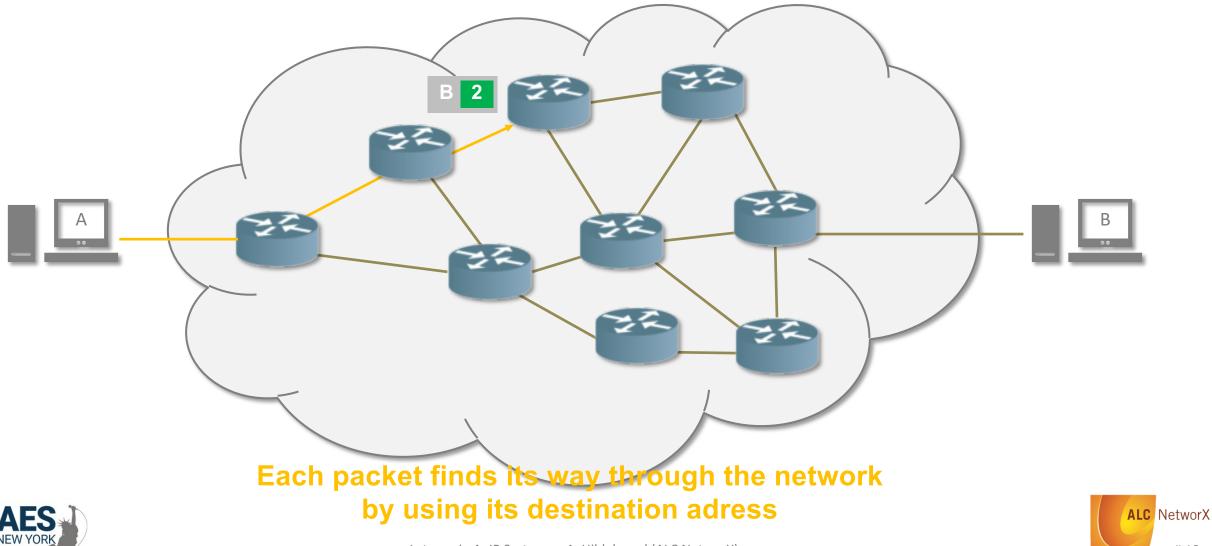
**IP Packet Switching** 



# 18

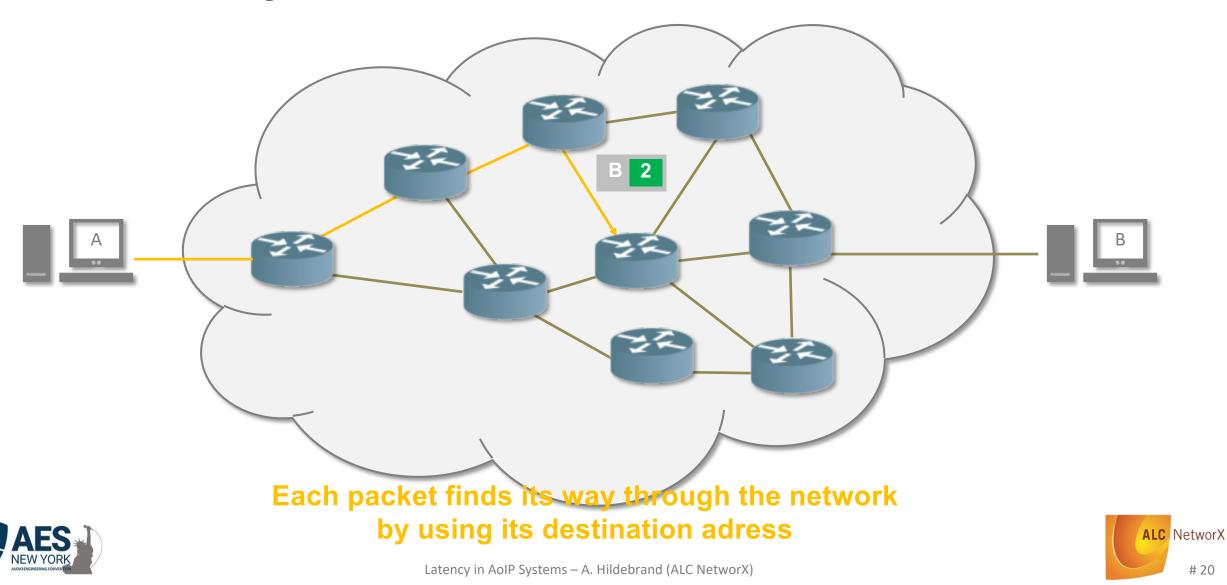


**IP** Packet Switching



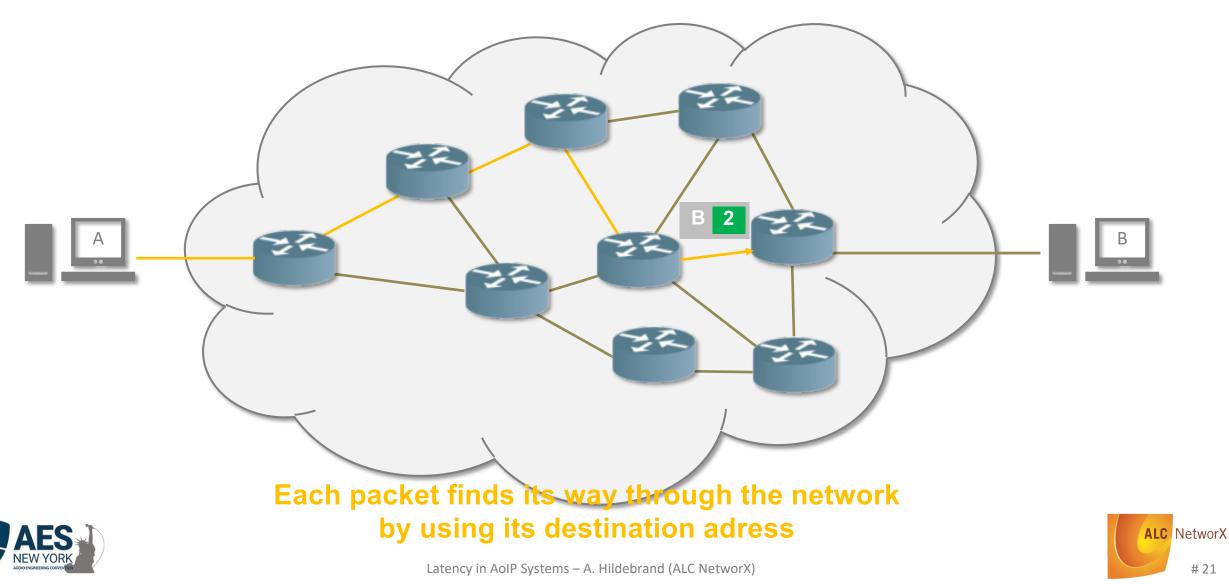


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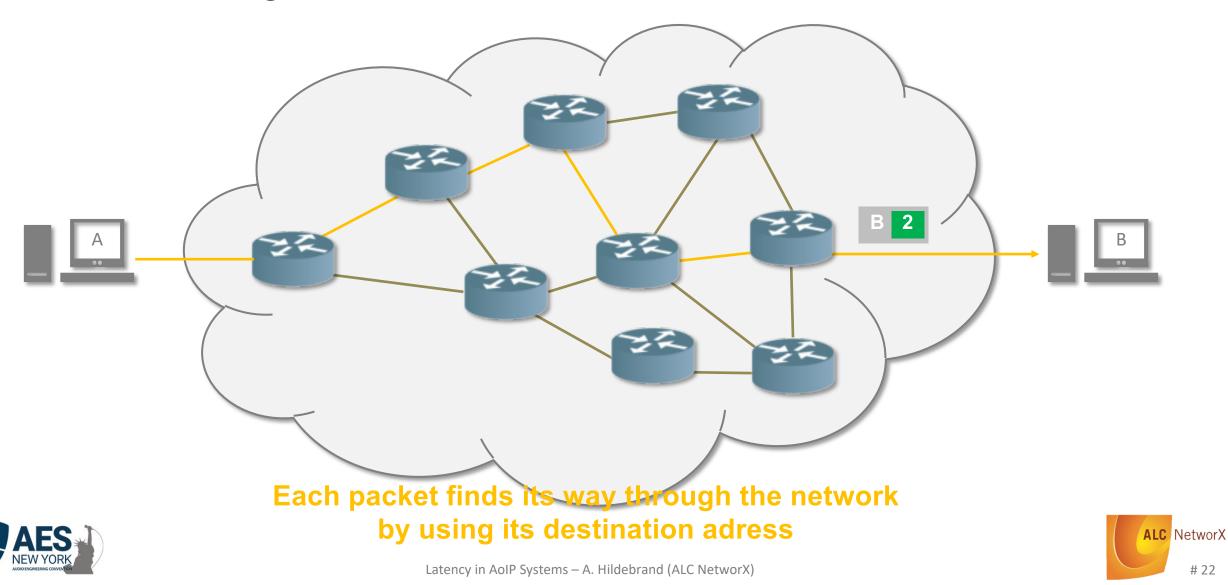


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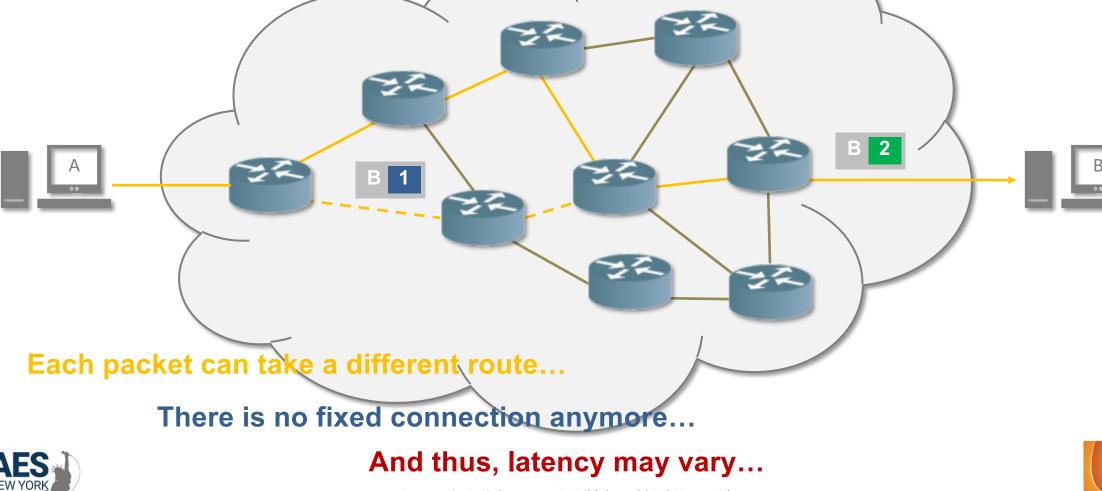




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**IP Packet Switching** 





• Underlying network technology

Network technology	Network speed	Frame transmission time (MTU)
FE	100 Mbit/s	258,58 μs
GbE	1 Gbit/s	25,86 µs (~ 1 sample time @ 48 kHz)
10G	10 Gbit/s	2,6 μs
40G	40 Gbit/s	10,4 μs
100G	100 Gbit/s	260 ns

- Type of switching / routing equipment
  - Hardware / switching fabric (switches, most routers)
  - Software (some routers / firewalls etc.)
  - Mixed (routers w/ firewalls and / or advanced processing / filtering functions)
  - Store-and-forward vs. cut-through







- Network topology
  - $\circ$  Number of hops
    - 1 hop @ GbE / switched: ~ 1 sample time @ 48 kHz
    - 4 hops @ FE / switched: ~ 1 ms
  - Unfortunately: delay not static due to dynamic traffic situation





- Network topology
  - $\circ$  Number of hops
  - Distances (speed of light)

Distance	Time
1 m	3.3 ns
1 km	3.3 μs
4000 km (NYC – LA)	13.3 ms
to geostationary orbit	119 ms
around Earth's equator	134 ms
Moon to Earth	1.3 s





- Network topology
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  - Distances (speed of light)

Distance	Time
1 m	3.3 ns
1 km	3.3 μs
6 km	20 μs (1 sample @ 48 kHz)
300 km	1 ms (mandatory AES67 packet time)
4000 km (NYC – LA)	13.3 ms
to geostationary orbit	119 ms
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Moon to Earth	1.3 s
3x round-trip Earth - Neptun	~ 1 d 51 m 19 s (RTP counter rollover @ 48 kHz)

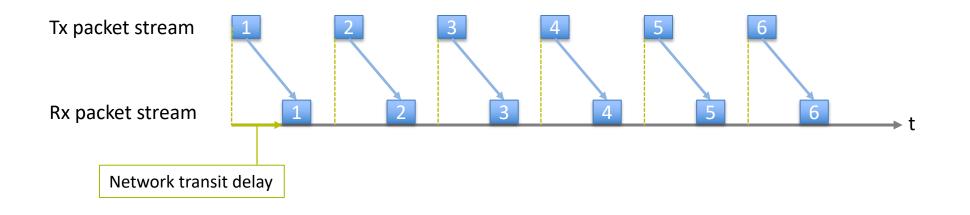


#### Latency in AoIP Systems



#### Latency – determining factors

• Network jitter ("PDV – packet delay variation")





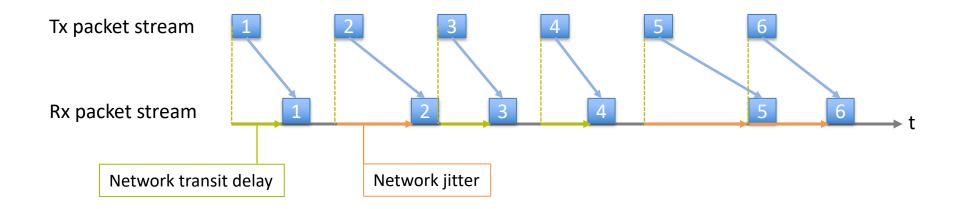


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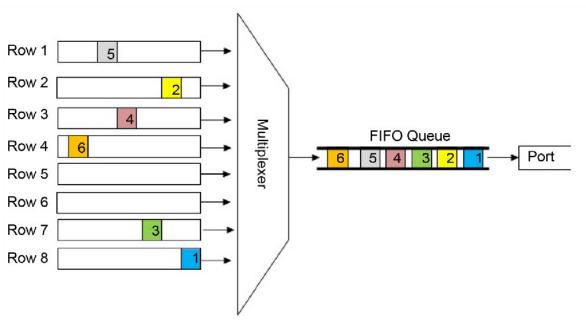


- Network jitter ("PDV packet delay variation")
  - switch performance (hardware / software)
  - o hops / routing (routing may change → no. of hops may change, network speed may change between hops)
  - $\circ$  bandwidth utilization





- Network jitter ("PDV packet delay variation")
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  - $\circ$  bandwidth utilization without QoS:









- Network jitter ("PDV packet delay variation")
  - switch performance (hardware / software)
  - o hops / routing (routing may change → no. of hops may change, network speed may change in between)
  - $\circ$  bandwidth utilization with QoS:





#### QoS – Differentiated Services (DiffServ)

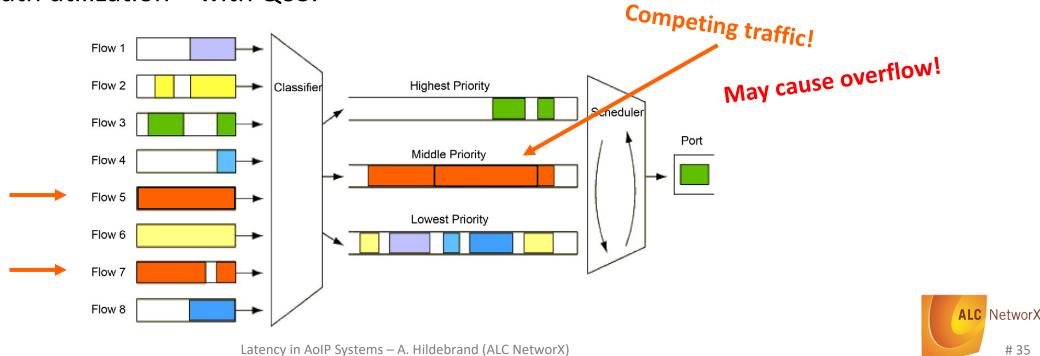




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- Network jitter ("PDV packet delay variation") ۲
  - switch performance (hardware / software) Ο
  - hops / routing (routing may change  $\rightarrow$  no. of hops may change, network speed may Ο change in between)
  - bandwidth utilization with QoS: Ο







#### Latency – worst case multi-hop calculations

	Bytes		100	1000	Mbit/s			
MTU RTP	1460		Net Data Rate Ethernet					
MTU IP	1472		98,70	987,03	Mbit/s			
MTU Ethernet	1500		Transfer Time					
Ethernet Frame Size	1518	$\longrightarrow$	123,04	12,30	μs			
Ethernet Frame Size w/ VLAN	1522	$\longrightarrow$	123,36	12,34	μs			
Preamble:	8							
Interframe Gap:	12							
Formula for max. Bridge	MaxLatency =	t BridgeDela	av + t MaxFra	me+n*t Str	eamFrame	n= number of	f concurrent RTI	O streams fro
t_BridgeDelay	inaxeatency =		10,24	_		in number of		sa cuns n
max. BridgeLatency			258,58	25,86	•	n= 1		
Max. Ethernet Frame Size	1522		230,30	23,00	μ5			
		1. Hop			2. Hop			3. Hop
	Bytes			Bytes Delay (μs)			Bytes	 Dela
1. stream	D y teo	FE	GbE	2,000	FE	GbE	2,000	FE
channels	8			8			8	
bytes / sample	3			3			3	
frames/packet	48			48			48	
sample rate	48000			48000			48000	
payload	1152			1152			1152	
frame size	1214			1214			1214	
frame + pre + IFG	1234			1234			1234	
→ Worst Case Latency		1713,12	171,31		3426,24	342,62		5139,36
Competing streams								
number	15			15			15	
channels	8			8			8	
bytes / sample	3			3			3	
frames/packet	48			48			48	
frames/packet sample rate	48 48000			48 48000			48 48000	





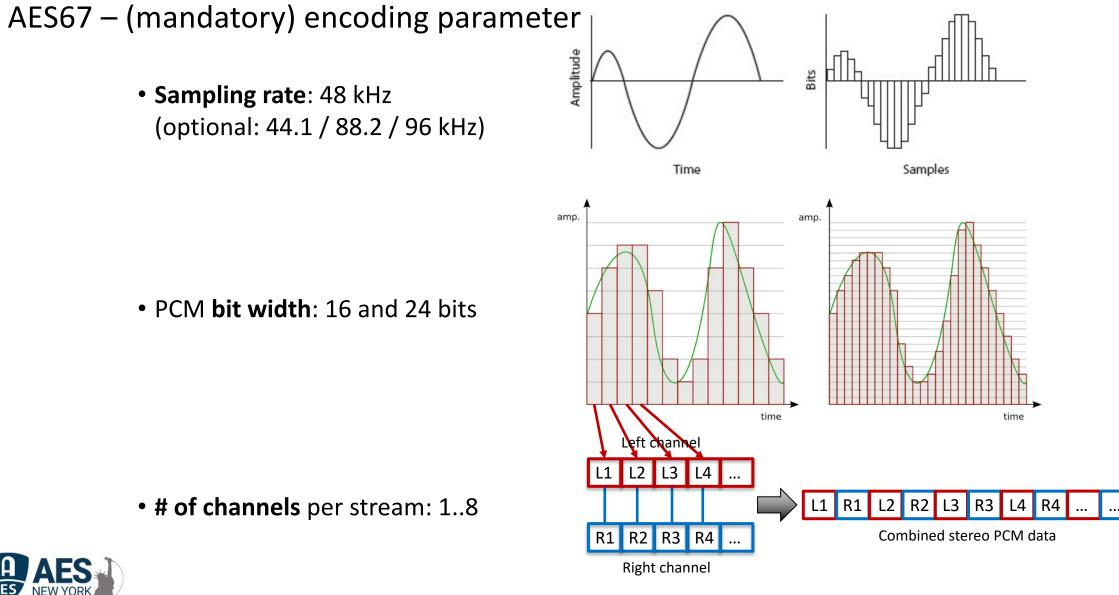
• Stream / packet configuration & sampling rate





#### Latency in AoIP Systems





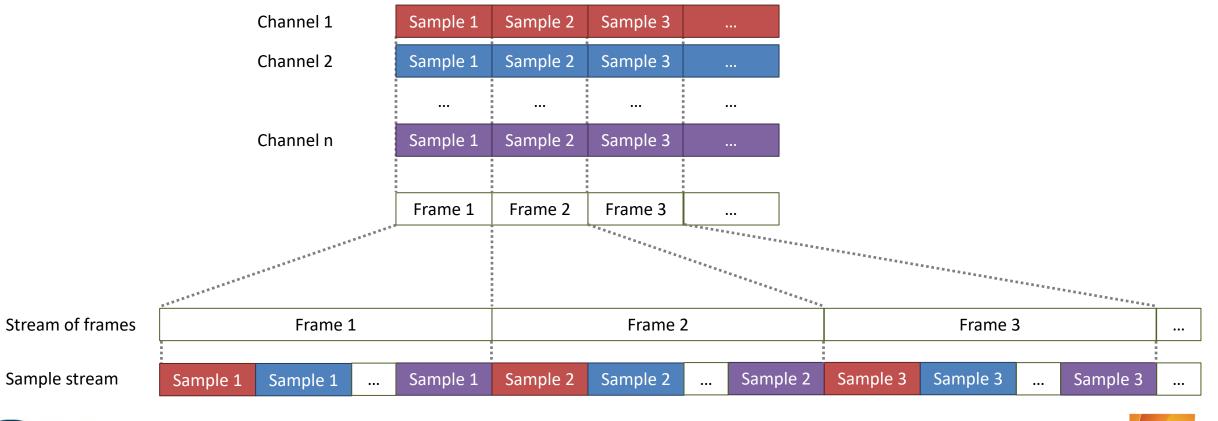
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#### AES67 – (mandatory) encoding parameter

• Packet time: # of frames (samples x channels) per packet at given sample rate





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#### Latency – determining factors

• Stream / packet configuration & sampling rate

	Bytes per sample	3		MTU-RTP	1460		Freq	48000
	Frames per packet	1	8	12	32	48	128	256
	Latency ms	0,02	0,17	0,25	0,67	1,00	2,67	5,33
Channels	1	3	24	36	96	144	384	768
per	2	6	48	72	192	288	768	1536
frame	3	9	72	108	288	432	1152	2304
	4	12	96	144	384	576	1536	3072
	5	15	120	180	480	720	1920	3840
	6	18	144	216	576	864	2304	4608
	7	21	168	252	672	1008	2688	5376
	10	30	240	360	960	1440	3840	7680





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#### Latency – determining factors

- Sender / receiver implementation
  - $\circ$  Hardware
  - $\circ$  Embedded
  - $\circ$  Software (VSC)

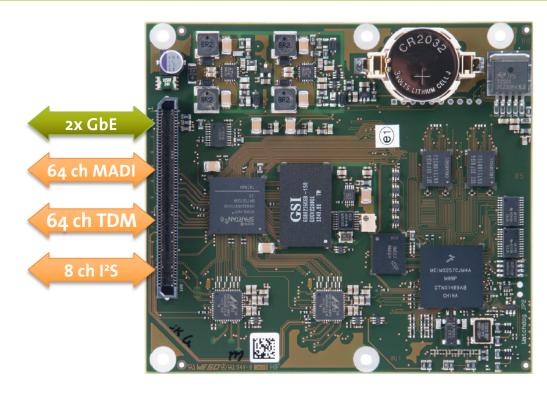


#### Latency in AoIP Systems

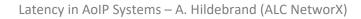




- Fully self-contained RAVENNA implementation
- Audio interfaces: I<sup>2</sup>S (8 ch) / TDM, MADI (64 ch)
- Up to 192 kHz sampling rate
- Lowest latency support: down to 1 sample/packet!
- 2 GbE NICs w/ ST2022-7 redundancy or load balancing
- 2x 64 channels in & out
- Full AES/EBU bit-transparent operation supported
- Jitter / delay buffer up to 40 ms per channel
- 4-tier 256 x 256 audio matrix
- Full AES67 & ST2110-30/-31 support

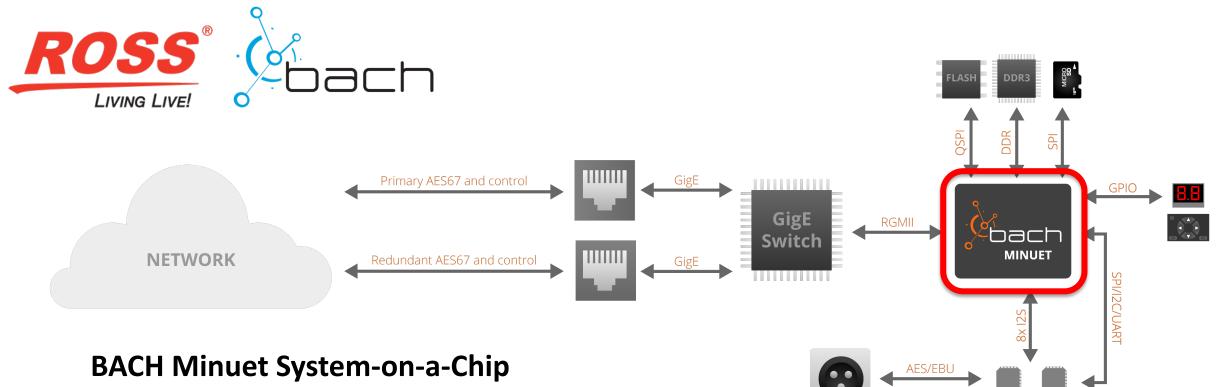






#### Latency in AoIP Systems





Highest performance SoC for small & medium channel applications

- Up to 16 channels
- 8 audio streams
- Cost-efficient for even the smallest 2 or 4-channel solutions
- Fully AES67 and ST 2110 standards compliant
- 2022-7 glitch-less redundancy



CODEC

ADC/DA



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#### VSC – Virtual Sound Cards

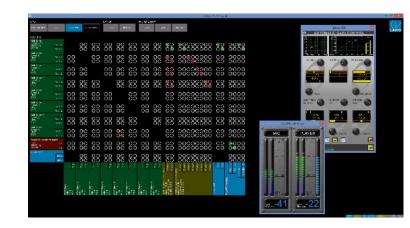


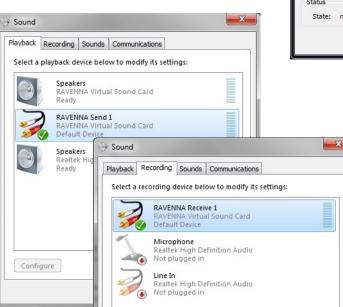


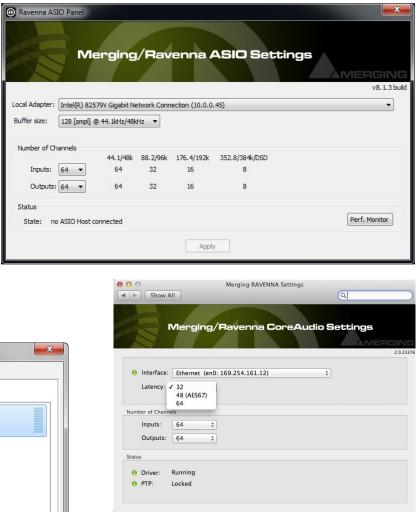




- Windows / MacOS / Linux
- Up to 64 channels playback / record
- Typ. processing latency: ~ 10 ms











#### Latency – determining factors

• Stream alignment

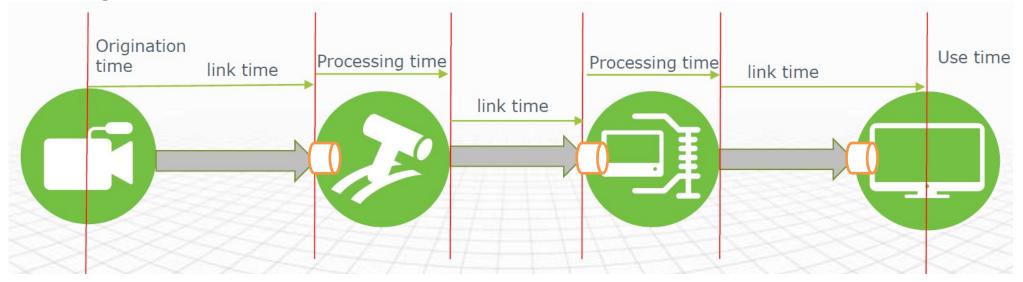




Image courtesy of Andy Rayner (Nevion)

Buffer





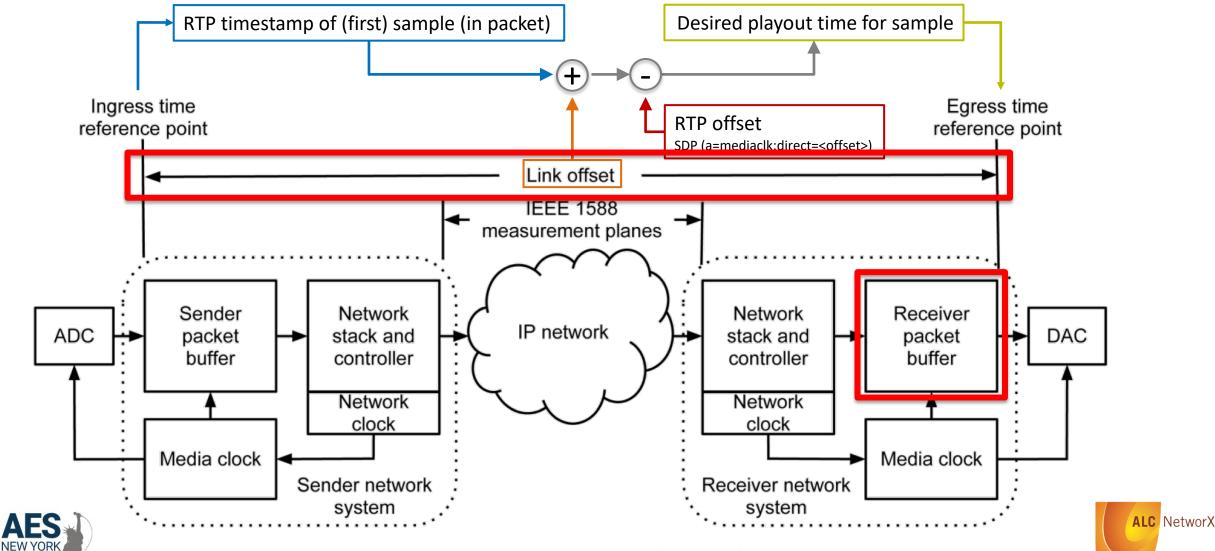
Latency – determining factors

 $\rightarrow$  Link offset & receiver buffer size





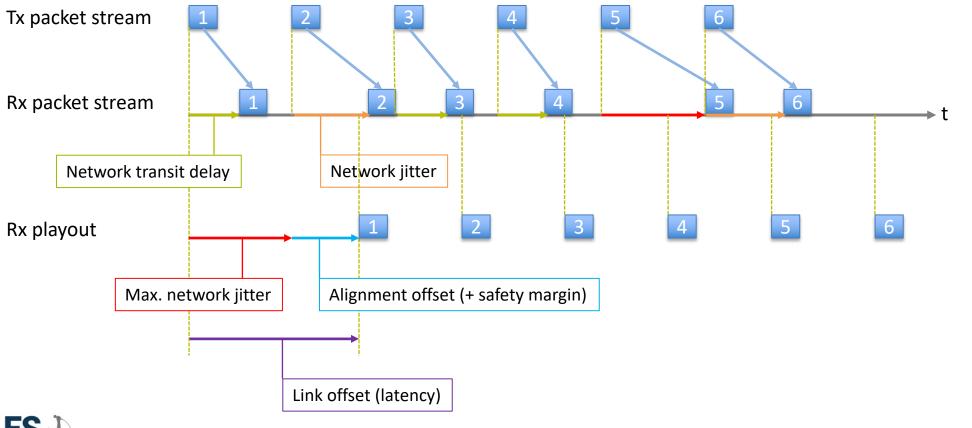
#### AES67 synchronization - link offset (latency)





#### Latency – determining factors

 $\rightarrow$  Link offset & receiver buffer size









Latency – determining factors

#### Global AES67 over WAN Demo





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#### Latency in AoIP Systems







#### AES67 over WAN - lessions learned

- Synchronization works via GPS-synchronized local GMs (no need to transport PTP)
- There is packet loss but this can managed via SRT
- Latency ranged from 200 to 600 ms
- Receivers need to have deep receive buffers or mechanisms to compensate for the network delay and PDV





#### Latency – determining factors (summary)

- Underlying network technology ullet
- Network topology ullet
- Network jitter (PDV) •
- Stream / packet configuration (packet time) ullet
- Sender / receiver implementation ۲
- Stream alignment •
  - $\rightarrow$  Link offset
  - $\rightarrow$  Receiver buffer size

Takeaways:

- ⇒ **sub-milliseconds** latency is achievable
- $\Rightarrow$  req'd receiver buffer size depends on application
  - AES67 requires 3 ms, but recommends 20 ms ullet

Receiver buffer size

Link offset (latency)





Latency in AoIP Systems



# **Questions?**







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## More information...

## RAVENNA / AES67 / SMPTE ST 2110 Resources:



www.ravenna-network.com/resources



www.aimsalliance.org (resources)

#### ravenna@alcnetworx.de





Latency in AoIP Systems – A. Hildebrand (ALC NetworX)







**Contact information:** 

Andreas Hildebrand ALC NetworX GmbH

ravenna@alcnetworx.de



#### www.ravenna-network.com







Supplemental Slides







#### Approximate light signal travel times

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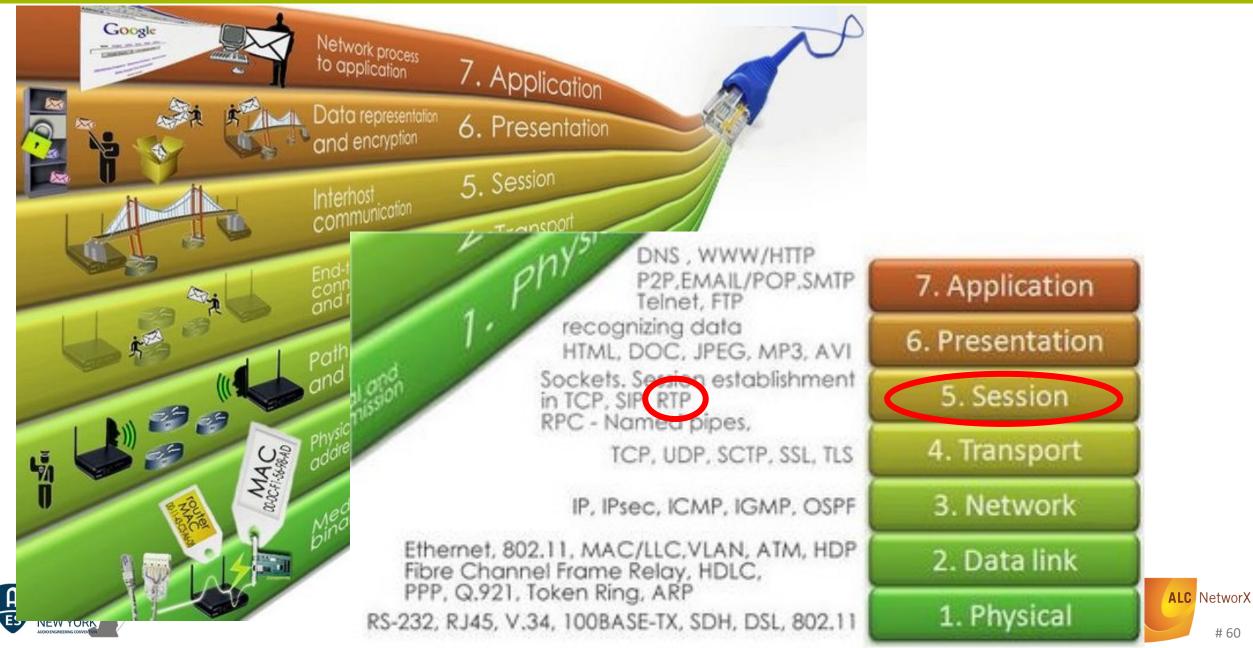




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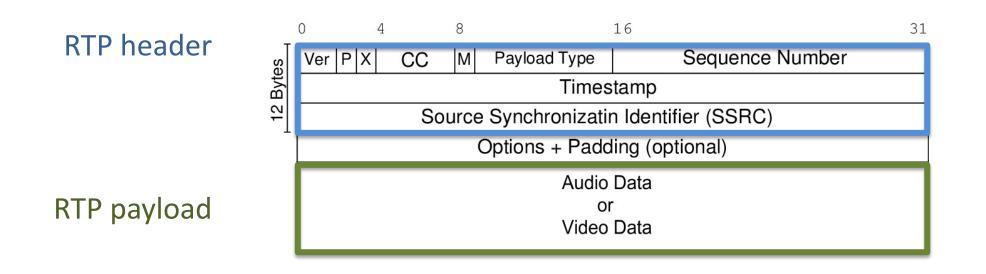
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## RTP Packets (Layer 5)

- A format-agnostic transport protocol for real-time media data (RFC 3550)
- Consists of an RTP header and a payload area







## RTP Packets (Layer 5)

- A format-agnostic transport protocol for real-time media data (RFC 3550)
- Consists of an RTP header and a payload area
- RTP header (overhead) = 12 Bytes
- IP + UDP + RTP overhead = 20 + 8 + 12 = **40 Bytes**
- MTU (maximum transmission unit, largest size of a packet that can be transmitted without being split): 1500 Bytes in an IP/Ethernet LAN
  - ⇒ 0 to 1460 bytes available for RTP payload data per packet

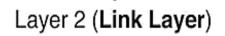






B y - t		1518 / 1522	•
e s	14/18	1500	4
	Ethernet Header	Ethernet Payload	Ethernet Trailer







Latency in AoIP Systems – A. Hildebrand (ALC NetworX)



B y - t	1518 / 1522				
e s	14/18	20	1480	4	
	Ethernet Header	IP Header	IP Payload	Ethernet Trailer	

Layer 3 (Network Layer)

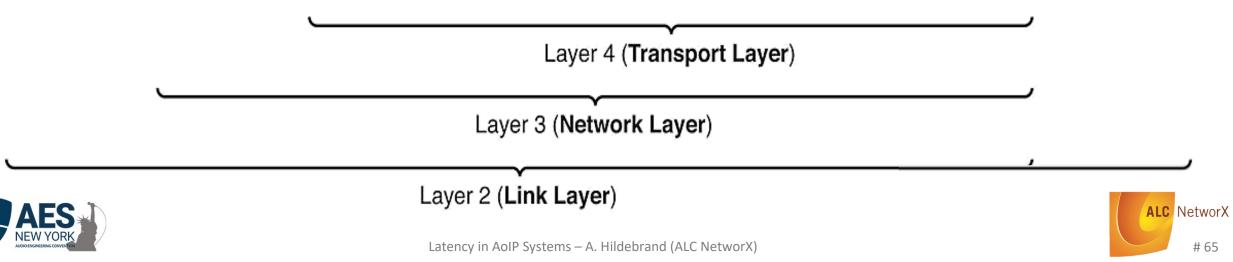


Layer 2 (Link Layer)

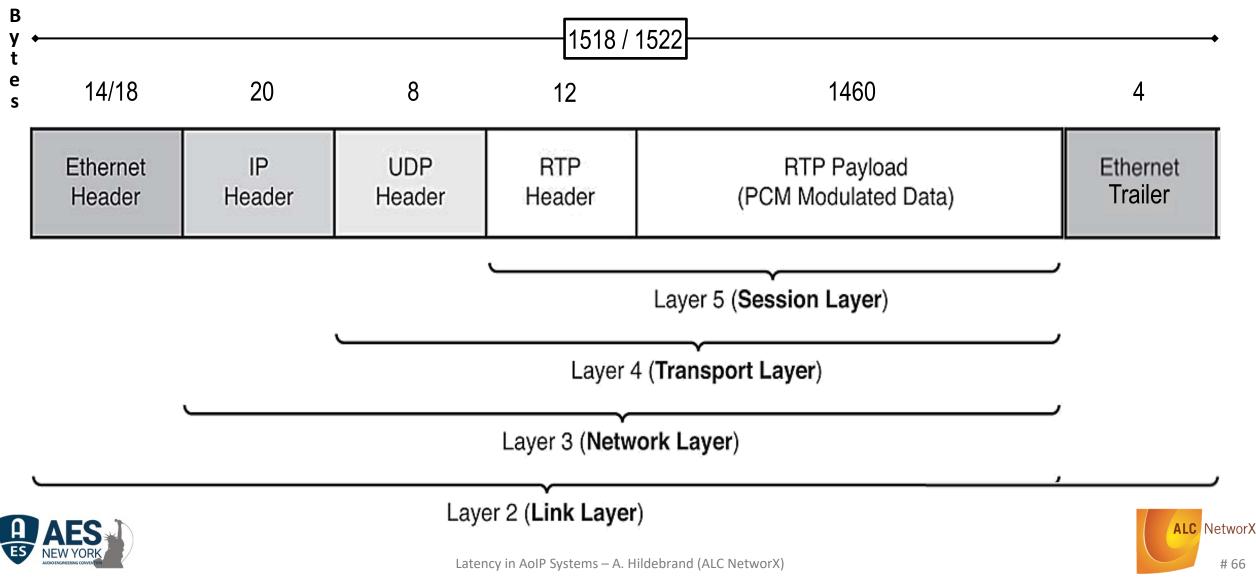
Latency in AoIP Systems – A. Hildebrand (ALC NetworX)



B y• t		1518 / 1522				
e s	14/18	20	8	1472	4	
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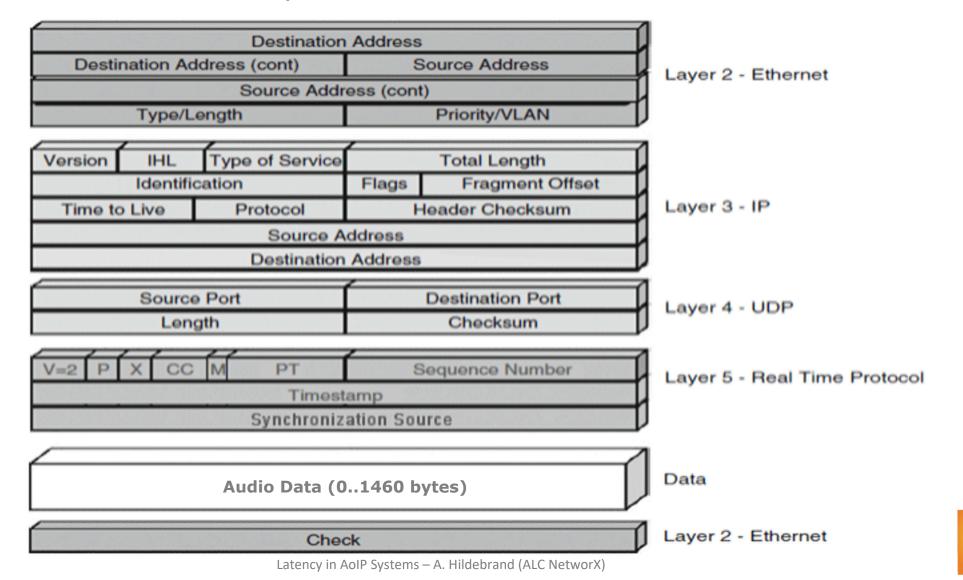




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#### **RTP** - Layered Packet Encapsulation







## **RTP Packets - Payload**

- Consist of RTP header, optional payload headers and the payload itself
- RTP overhead = **12 Bytes**
- IP + UDP + RTP overhead = 20 + 8 + 12 = **40 Bytes**
- MTU (maximum transmission unit, largest size of a packet that can be transmitted without being split) 1500 Bytes in an IP/Ethernet LAN: in principle
   0 to 1460 bytes available for RTP payload data per packet
- Examples:

#1: 16 bit PCM, 2 channels, 96 samples (2 ms @ 48kHz): 384 bytes
#2: 24 bit PCM, 8 channels, 48 samples (500 μs @ 96kHz): 1152 bytes
#3: AES3 24 Bit PCM, 64 channels, 5 samples (104 μs @ 48 kHz): 1280 bytes







AES67 technology components:

- Network: IPv4 (IPv6), unicast / multicast & IGMPv2
- Transport: RTP/AVP (RFC 3550 & 3551) / UDP / IP
- Quality of Service: DiffServ w/ 3 suggested traffic classes (DSCP)





# Quality of Service





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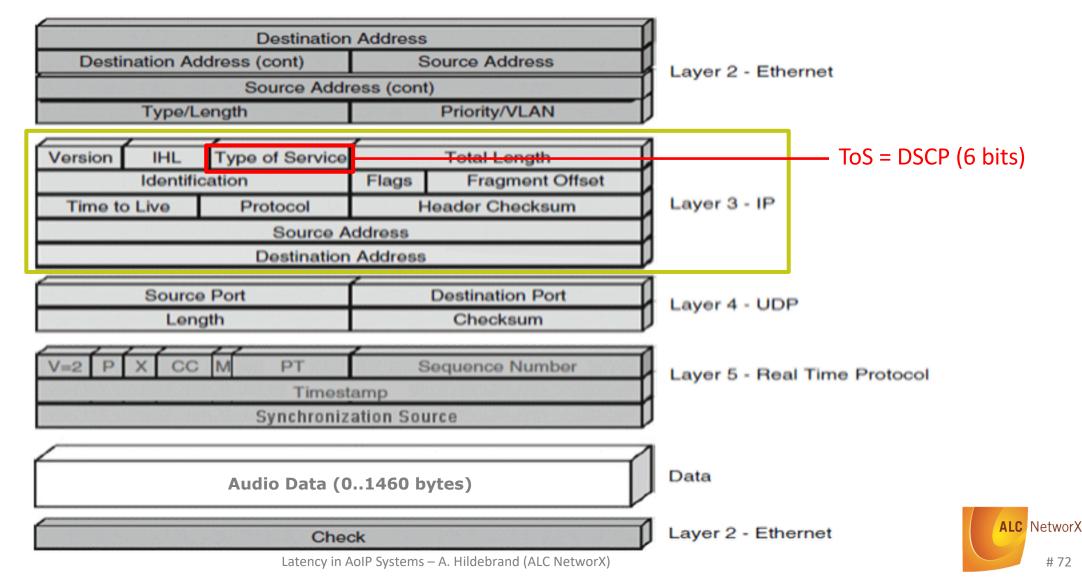
Latency in AoIP Systems – A. Hildebrand (ALC NetworX)



- Defined in RFC 2474
- Defines up to 64 traffic classes (i.e. EF, AFx, CSx, BE etc.)
- Packets are tagged with DSCP value (0 63)

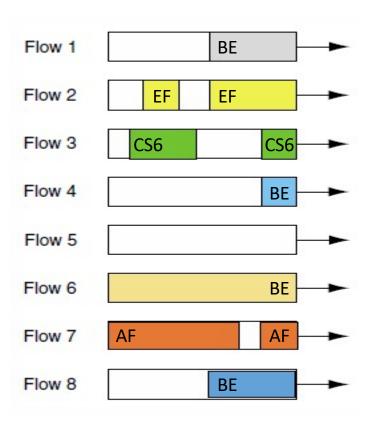












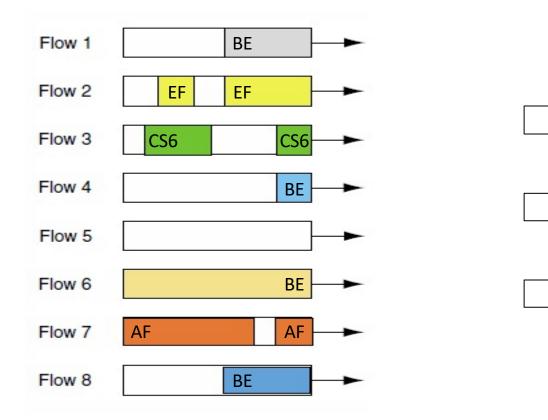




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- Switches store packets in different priority queues (requires proper configuration)







Queue 1 (High Priority)

Queue 2 (Medium Priority)

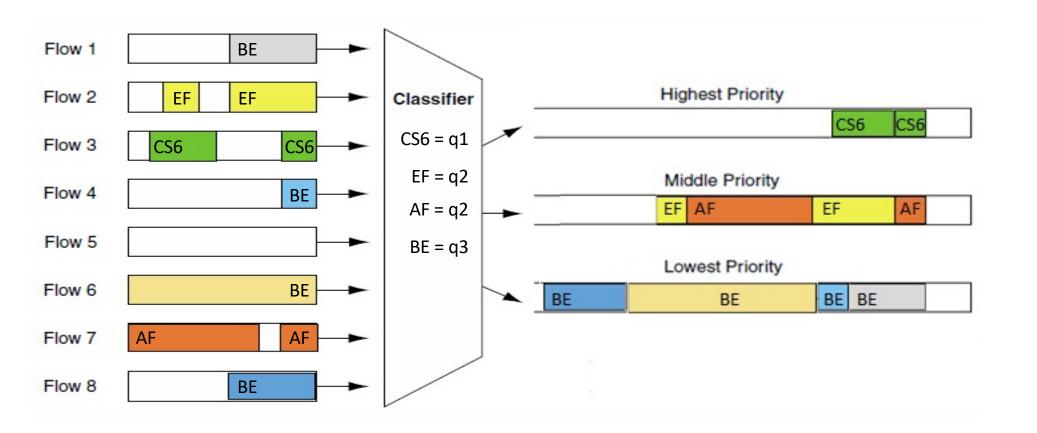
Queue 3 (Low Priority)



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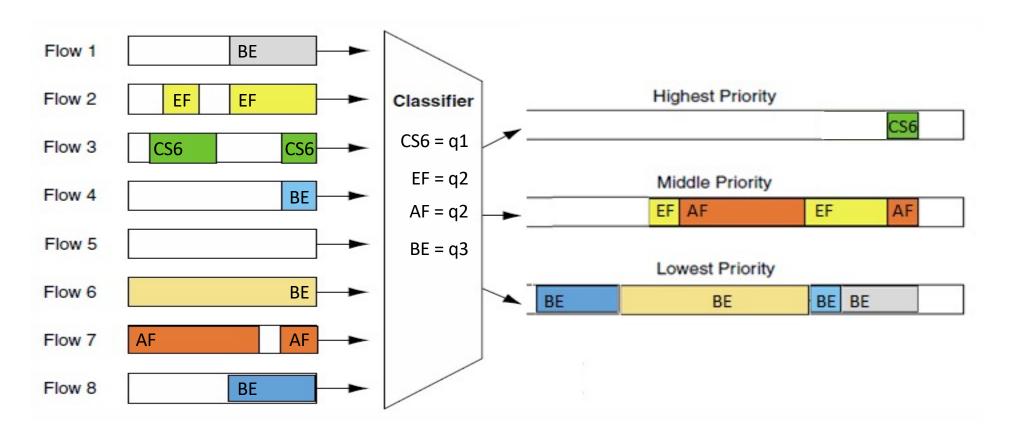


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- Egress scheduler forwards packets from higher prioritized queues first (strict priority / weighted round robin / guaranteed minimum bandwidth ...)





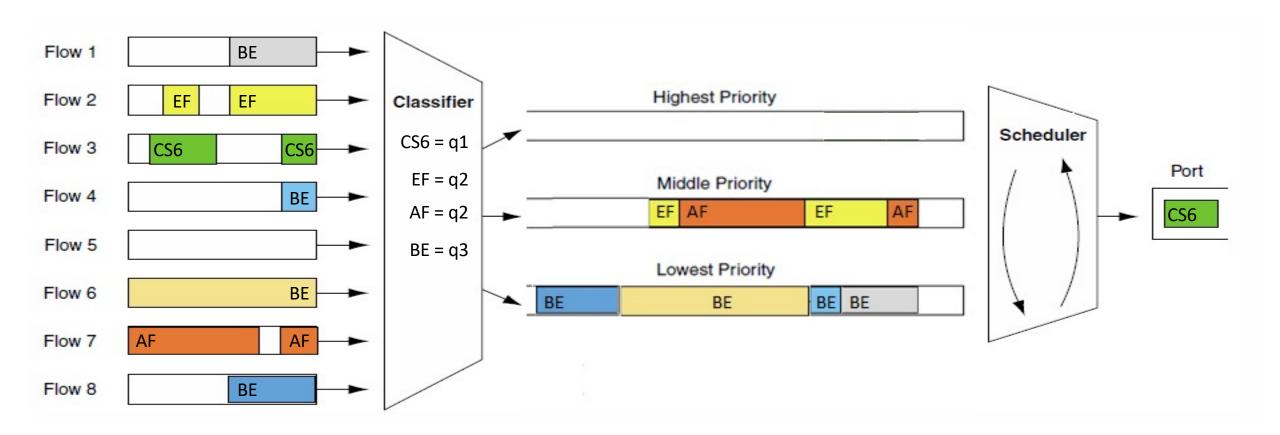






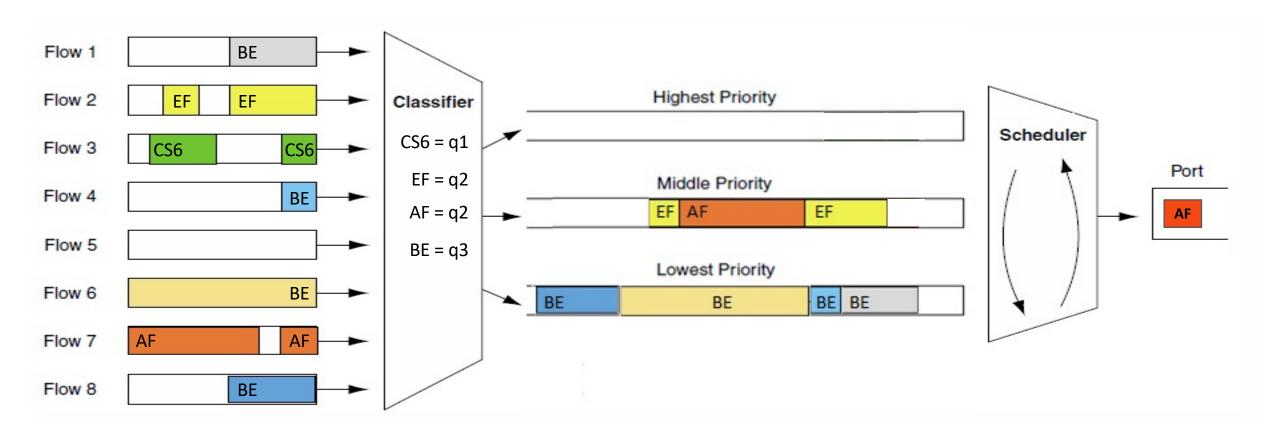
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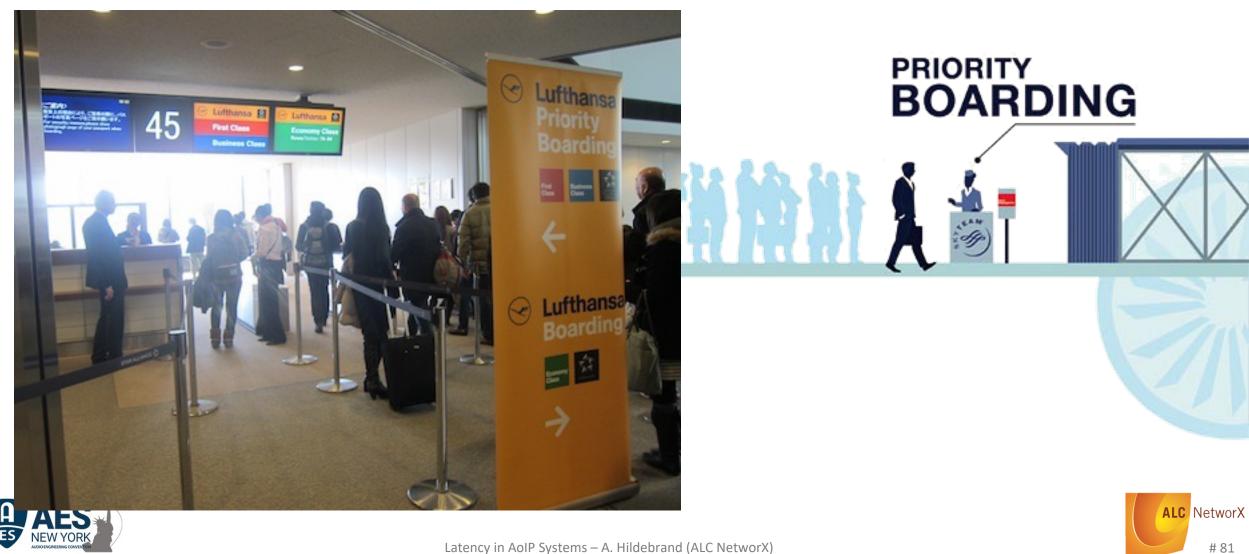














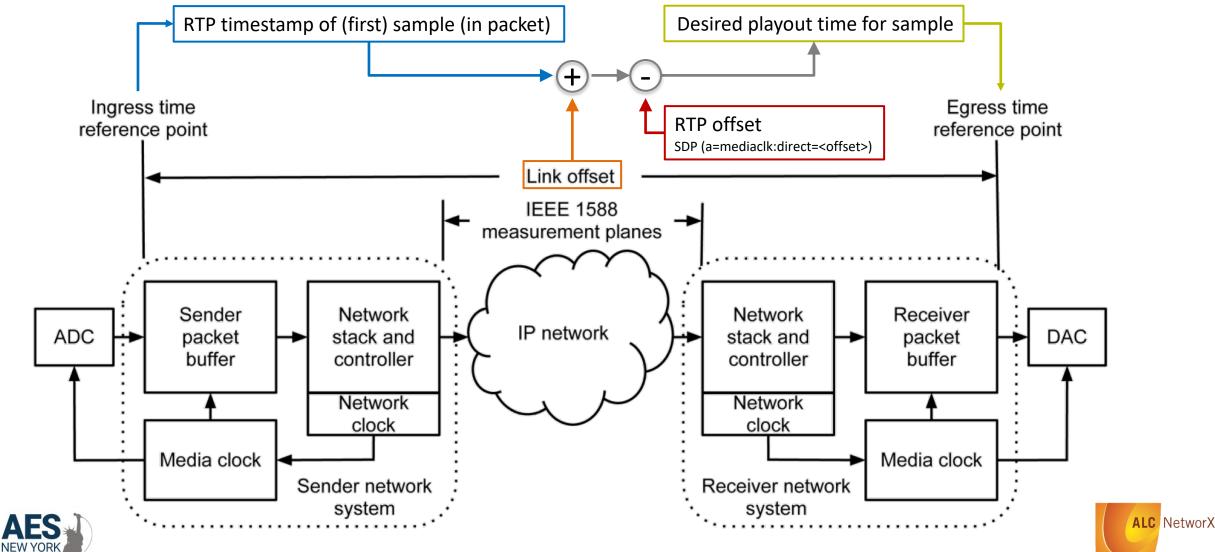


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- Egress scheduler forwards packets from higher prioritized queues first (strict priority, weighted round robin, guaranteed minimum bandwidth)
- Needs to be supported along full path from the transmitting to the receiving end
- No admission control → congestion / packet dropping possible when bandwidth is exceeded





#### AES67 synchronization - link offset (latency)



Latency in AoIP Systems – A. Hildebrand (ALC NetworX)





#### Production Workflow Timing

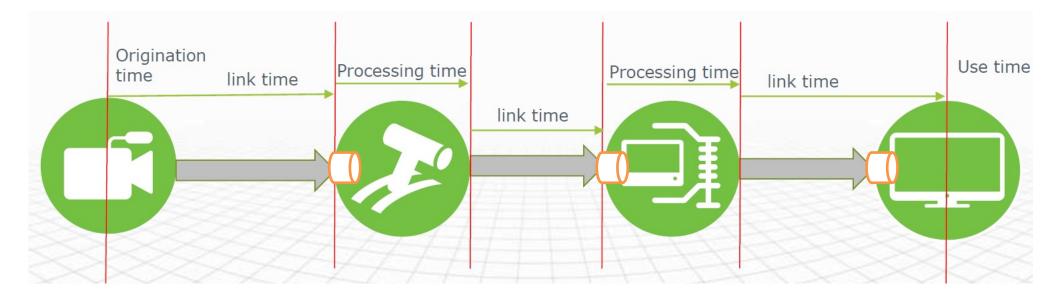




Image courtesy of Andy Rayner (Nevion)

Buffer

